

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Jerome M. Eldridge et al.	Examiner:	Tu-Tu Ho
Serial No.:	10/081,818	Group Art Unit:	2818
Filed:	February 20, 2002	Docket:	1303.045US1
Customer No.:	73115	Confirmation No.:	3148
Title:	ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPLAY INSULATORS		

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR 1.97(i)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner in connection with the above-identified patent application. Since a Notice of Allowance and Issue Fee Notification have been issued, Applicants understand that the enclosed materials will be placed in the file and not considered by the U.S. Patent Office.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement.

Applicants bring to the attention of the Examiner the following Office Action associated with one or both of the inventors of the instant application and/or the assignee of the instant application. This material is available and accessible at the United States Patent and Trademark Office.

**GERMANIUM-SILICON-CARBIDE FLOATING GATES IN MEMORIES**

Application Serial No. 11/731,255 (Atty. Ref. 1303.132US3 ) Final Office Action mailed 11/03/2008., 15  
pgs

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The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

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Date

7 Nov '08

By



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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 7 day of November, 2008.

Name



Signature

